	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	4936	instruction adj cache and level	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/21 13:05	
2	BRS	L2	1435	instruction adj cache SAME level	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/21 13:05	
3	BRS	L3	52	instruction adj cache SAME level SAME instruction adj buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/21 14:16	
4	BRS	L4	52	instruction adj caches SAME level SAME instruction adj buffer	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/21 13:06	
5	BRS	L5	0	"L2 I-cache"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/21 14:17	
6	BRS	L6	0	second near2 (insteruction adj cache)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/21 14:18	
7	BRS	L7	237	second near2 (instruction adj cache)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/21 14:28	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
8	BRS	L8	6	7 and 4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/21 14:18	
9	BRS	L9	24039	7 ans (way WITH set WITH associate WITH cache)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/21 14:29	
10	BRS	L11	0	10 and 8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/21 14:29	
11	BRS	L10	18	7 and (way WITH set WITH associate WITH cache)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/21 14:29	